

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
29 December 2004 (29.12.2004)

PCT

(10) International Publication Number
WO 2004/114269 A1

(51) International Patent Classification⁷:
G06F 3/00, G02F 1/133, H03K 19/0175

G09G 3/20,

(74) Agent: LEE, Cheol-Hee; 203, Yuseong Bldg, 668-16, Yeoksam-dong, Kangnam-ku, Seoul 135-080 (KR).

(21) International Application Number:
PCT/KR2003/001813

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date:
4 September 2003 (04.09.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10-2003-0040538 23 June 2003 (23.06.2003) KR

(71) Applicant (for all designated States except US): SYN-COAM CO., LTD. [KR/KR]; 512, Korea Design Center, Yatap-dong, Bundang-gu, Seongnam-si, Gyeonggi-do 463-954 (KR).

**(71) Applicant and
(72) Inventor:** JEONG, Seong-Ik [KR/KR]; 202, Chung-woan-Silverbill, 262-6, Yangjae-dong, Seocho-gu, Seoul 137-130 (KR).

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

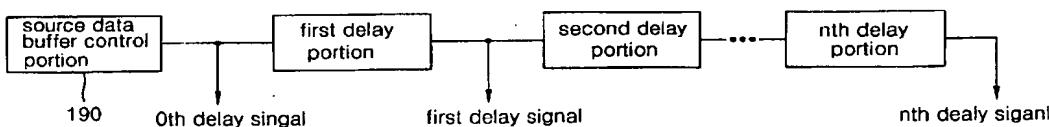
— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.



WO 2004/114269 A1

(54) Title: MEMORY CIRCUIT FOR DISPLAY PANEL DRIVING AND DRIVING METHOD THEREOF



(57) Abstract: The invention provides a circuit driving method which minimizes a problem generated by a high peak current in a display memory device. To reduce the peak current, the output lines of data-buffer (buffers are connected to the corresponding memory cells) are grouped, and each group is activated with predetermined time intervals, instead of being simultaneously driven. The mentioned time intervals are made with time-delay components.